

Design and Synthesis of Artificial Neural Network using HDL

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Abstract - In this paper, a Single Layer Perceptron for AND function has been designed and synthesized. The code is designed in MATLAB 13 and synthesized in Xilinx .It has been trained using Perceptron Convergence Algorithm. The results are compared with NNTOOL (Neural Network Design and Development Toolbox)available in MATLAB 13.The results shows that code designed in MATLAB gives exact output without any errors and NNTOOL gives output with errors present.

Key Words: Artificial Neural Network, Single Layer Perceptron, Perceptron Convergence Algorithm

1. INTRODUCTION

An Artificial Neural Network (ANN) is an information processing paradigm that is inspired by the way biological nervous systems, such as the brain process information. The advances in the VLSI research and the performances attainable by Integrated circuits (IC) have risen to the great interest in the VLSI implementation of ANN, when going for implementation of single quantity, ASIC implementation becomes too much costly than implementation on FPGA. Advances in FPGA technology in last decade, make implementation of ANN on FPGA more interesting and easier than ASIC.

1.1 Overview of Artificial Neural Network

The Artificial Neural Network (ANN) is inspired by the 'Biological Neuron'. In Biological Neural Network, a cell or Neuron receives responses from other neurons via dendrites. When cell body contains enough action potential from dendrites, it fires and sends activation signal via axon to the dendrites of other neuron.

Same way, Artificial Neural Network has been conceptualized. It gets electrical signal from input and their summation is passed through a threshold function. If the result of summation exceeds the threshold than the neuron 'fires' that is the output of threshold function will be positive else output of threshold function will be negative. In case when actual output is different from desired output then weights and bias are set. So when same pattern will come it will produce correct output. It is shown in fig 1.

1.2 Design of Single Layer Perceptron

Definition: "A perceptron is a type of Neural Network which computes a weighted sum of its inputs and puts this sum through a special function, called the activation, to produce the output". The activation function can be linear or nonlinear [1].



Fig -1: Block diagram of Artificial Neural Network [1]

Single Layer Perceptron can classify input vector into linearly separable classes. The perceptron convergence algorithm is used for training of Single Layer Perceptron.



Fig -2: Block diagram of Single Layer Perceptron.

A figure shows the block diagram of Single Laver Perceptron which is designed for AND functions. Input of perceptron used for AND function is 3x8 matrix. Which is X=[1 1 1 1 -1 -1 -1 -1; 1 1 -1 -1 1 1 1 -1 -1; 1 -1 1 1 -1 1 -1]; Target matrix for AND function is T = [1 -1 -1 -1 -1 -1 -1 -1]; The updating of weights and bias process follows throughout



n algorithm which is known as perceptron convergence algorithm. Variables and Parameters are as below:

X(n) = (m+1)-by-1, input vector $= [+1, x1(n), x2(n), ..., xm(n)]^{T}$ b(n)=bias

y(n)=actual response

d(n)=desired response

 η =learning parameter, a positive constant less than unity.

Here, Perceptron gets input from input matrix and maps actual output (which is initially taken as zero) with target output. The goal is to minimize the generated error signal which is nothing but the difference between actual output and target output. In this paper, the error signal becomes zero within only four iterations for learning rate= 1 and threshold = 0. For varying learning rate and threshold the algorithm takes different number of epochs to converge.

2. Synthesis result

Here, Single Layer Perceptron for AND function is designed in MATLAB. It is synthesized in Xilinx and obtained VHDL code.

Name	Value		240 ns	260 ns	280 ns	300 ns	320 ns	340 ns	360 ns
🔓 cik	0								
🕼 reset	0								
16 clk_enable	1								
▶ 🔩 data1[2:0]	1		-1	X	1	x	-1	X	1
🕨 👹 data2[2:0]	-1	1	χ -1	χ1	χ -1	<u>\ 1</u>	χ -1	χ1)1
🕨 👹 data3[2:0]	1	1 -1	X 1 X -1	(1)(-1	(1)(-1	<u> </u>	(1)(-1	(1)(-1	
🕨 🐝 t1[2:0]	-1		-1))		-1))	
▶ 🐝 theta[1:0]	2						2		
🔓 alpha	1								
🕨 🐝 w1[3:0]	3	0	1	X					3
🕨 📲 w2[3:0]	3	2	1						3
▶ 🔩 w3[2:0]	3	2	1						3
▶ 🔩 b[3:0]	-6	-6	-7						-6
🕼 ce_out	1								
▶ 🔩 y1[2:0]	-1	0 /			-1)()()	
▶ 🔩 e1[2:0]	0	-1	0) 2)					0
▶ 🍇 w1new[3:0]	3		1	X				3	
▶ 號 w2new[3:0]	3		1	X				3	
▶ 🔩 w3new[2:0]	3		1	χ				3	
▶ 🐝 bnew[3:0]	-6		-7	X				-6	

Fig- 3: Simulation result of Single Layer Perceptron for AND function

Here, 'data 1', 'data 2' and 'data 3' are the inputs. 't1' is target output of perceptron. 'y1' is the actual output, which is initially zero and after one epoch it becomes equals to target output. Here 'e1' is the error signal. 'Alpha' is learning rate which is set to 1 and 'theta' is threshold which is set to zero. 'w1', 'w2' and 'w3' as shown in fig 3.'b' is the bias (fixed output). 'w1new', 'w2new' and 'w3new', 'bnew' are updated values of weights and bias respectively. When weights are not updated error is present but when weights are updated error becomes zero.

Final weights are [w1, w2, w3] = [333]

Bias = [-6]

The RTL schematic of obtained result is shown in fig 4. It shows the pins and its functions.



Fig -4: RTL schematics of Single Layer Perceptron for AND function (Top view)

3. Comparison with NNTOOL

MATLAB has Neural Network Design and Development Toolbox (NNTOOL) which is used extensively for designing of Neural Network. Using that block diagram of Single Layer Perceptron is obtained.



Fig -5: MATLAB generated block diagram of Single Laver Perceptron from NNTOOL

Single Neuron is used in this block diagram. The disadvantage of NNTOOL output is that more number of weights and bias are used. And even after 1000 epochs error is not zero.



Fig- 6: Error graph generated from NNTOOL

In NNTOOL number of weights and bias generated are as shown.

Weight matrix after 1000 epochs= [6198 6198 6198];

Bias after 1000 epochs = [-18604];

The Mean Absolute Error is 1.

4. CONCLUSIONS

In this paper, Single Layer Perceptron is designed in MATLAB¹³ using 3 input bits one output bit and single neuron. Here first, Single Layer Perceptron is designed in MATLAB then after MATLAB code is converted into VHDL by using HDL coder available in MATLAB¹³. We have synthesized in Xilinx and got the RTL schematics and corresponding clock waveforms.

We have overcome the disadvantage of NNTOOL (Neural Network Design and Development Toolbox) used for Neural Network in which more number of weights and bias are used and error is present even after 1000 epochs. Whereas in our algorithm same process is done by using only 3 weights and 1 bias within only 4 iteration and error has become zero.

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