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Design and Analysis of Modular Multilevel Inverter for PV-FED Applications

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Abstract - In this project, the solar fed cascaded multilevel inverter which is used to reduce the number of semiconductor Switches. The 'binary', 'trinary' and modified multilevel connection' is based on topologies which are suitable for varying input sources from solar photovoltaic's (PV). In the binary mode, 2Ns +1 – 1 output voltage levels are obtained by Ns which is the number of the individual inverters. This is achieved by digital logic functions that include counters, flipflops and logic gates. In trinary mode, 3Ns levels are achieved by corresponding look-up table. The modified multilevel connection intends design in both control and as well as power circuits to provide corresponding output voltage levels by appropriate switching and sequences. Hence, A 15-level inverter, the conventional method requires to 28 switches and in binary mode to 12 switches are needed. In the trinary mode with the same 12 switches 27 levels can be obtained where in modified multilevel connection only have 7 switches are employed to achieve the 15 levels. Now adding one more photovoltaic's panel and one more switches in modified multilevel connection are employed to achieve 31 levels with the help of binary mode. The advantage of these three designs which is in the reduction of the total harmonic distortion by increasing the levels offer sinusoidal output wave forms, lower EMI, and low switching losses and also reduced THD value. Here Simulations are carried out into the MATLAB/Simulink and comparisons were made. So the experimental results have been implemented by using the PIC Microcontroller in 15 levels only because if here going to implement on 31 levels the wave for become sinusoidal wave form.

Key Words: Photovoltic, Modified Multilevel Connection

1. INTRODUCTION

Multilevel inverter provides a suitable solution for medium and the high power systems to synthesize an output voltage which can allows a reduction of the harmonic content in the voltage and current wave forms. Renewable energy power which supplied into the utility grid has been paid much attention due to the increase in fossil fuel prices, environmental pollution and the energy demand boom. Among various renewable energy resources such as solar, wind, tidal, geothermal, biomass etc., In the solar photovoltaic system is more attractive and the promising green resource. The solar photovoltaic modules directly can converts the light energy into the electrical energy, but the energy obtained from the photovoltaic module which acts like low voltage DC source and has relatively low conversion efficiency. In order to improve the efficiency and the convert low voltage DC source into usable AC source, the power electronics converters are used to transform the DC into AC. In the simulation results presented in this paper which verifies the operation of proposed in modified multilevel connection topology.

2. PROBLEM STATEMENT

To obtain the 15-level inverter, the conventional method requires 28 switches and in the binary mode 12 switches are needed. In the trinary mode with the same 12 switches, 27 levels can be obtained whereas in the modified multilevel connection only 7 switches are employed to achieve the 15 levels. The advantage of these three designs are in the reduction of total number of harmonic distortion by increasing the levels.



Fig -1: Existing circuit diagram

In this approach, the modification is made in both of the control circuit and the predominately in power circuit to obtain the 15 levels with only seven switches. The addition of diode and the capacitor (1nF) is use to normalize the output within the given interval. Let the photovoltaic array inputs be V1–V3. During the level 1, V1 alone is given as input to the inverter and V2, V3 in OFF condition. Similarly the 15 level is achieved by the controlling the ON/OFF status of the input voltages in the hardware.herw have some of disadvantages

like low power quality, much higher harmonics, poor efficiency

2.1 Existing Simulation



Fig -2: Existing simulation



Fig -3: switching table of 15 level

2.1.1 Simulation Output



Fig -4: existing simulation output

2.2 FFT analysis existing simulation



Fig -5: FFT analysis

3. METHODOLOGIES

A tool for component sizing for modified multilevel connection has been developed and tested through simulations in PLECS. This is the steady-state behavior under grid frequency deviations - interesting for off shore photovoltaic panel's connections which has been analyzed, providing insights in modified multilevel connection characteristics and further testing for the proposed tool.

3.1 Switching strategies

In this approach, PWM techniques which are used by the comparison of reference and carrier signals to provide the required gating signals for the inverter switches.

3.1.1 Binary mode

In the binary mode operation, the number of the levels which can be achieved by the given set of inverter stages which is given in the following equation: (m = 2Ns+1 - 1)hence to obtain the 15-level output, only three inverter stages are required for 12 switches. To achieve this, the switching circuit with the control strategy incorporating digital logic functions is use to implemented for the solar fed CMLI.So the three inverter stages which are fed from varying solar photovoltaic input source. Hence the input voltages are use to scaled for the power of 2 in order to achieve by the output voltage in the range of 2Ns which can be made by the possible by binary counters. An incremental and descended operator is required to the achieve the condition 2Ns + 1 - 1at the output level. At the positive half cycle the switches S1, S5 and S9 are in ON condition and during negative half cycle S3, S7 and S11 are in ON condition. The conduction period for each of the half cycle is fixed at 10 ms which will determine by the output frequency of the inverter as 50 Hz.

3.1.2Trinary mode

In the trinary mode operation, the number of levels which can be achieved by the given set of inverter stages which is given in the following equation :(m = 3Ns) with the three inverter stages for 27 levels can be obtained by only 12 switches. To achieve this, rather than the digital logic functions used in the binary mode, an embedded by controller which is proposed without the utilization for transformers and the complicated algorithms. The switching sequence for the switches S1 to S12 which use to obtain 13-level output during the positive half cycle and in the negative half cycle at the same sequence is rotated by an angle of 90° to achieve the remaining 13 level. By including the level zero, to the desired 27 levels will be achieved.



Fig -6: Three stage inverter power circuit

Level	S 1	S2	S3	S4	S5	S6	\$ 7	S8	S9	S10	S11	S12
1	1	0	0	1	1	1	0	0	1	1	0	0
2	0	1	1	0	1	0	0	1	1	1	0	0
3	1	1	0	0	1	0	0	1	1	1	0	0
4	1	0	0	1	1	0	0	1	1	1	0	0
5	0	1	1	0	0	1	1	0	1	0	0	1
6	1	1	0	0	0	1	1	0	1	0	0	1
7	1	0	0	1	0	1	1	0	1	0	0	1
8	0	1	1	0	1	1	0	0	1	0	0	1
9	1	1	0	0	1	1	0	0	1	0	0	1
10	1	0	0	1	1	1	0	0	1	0	0	1
11	0	1	1	0	1	0	0	1	1	0	0	1
12	1	1	0	0	1	0	0	1	1	0	0	1
13	1	0	0	1	1	0	0	1	1	0	0	1

Fig -7: ON/OFF stage of 27 levels

3.2 Solar Energy

In the today's climate of the growing energy needs and increasing the environmental concern, alternatives to the use of non-renewable and polluting fossil fuels have to be investigated. One such alternative is solar energy. An array is the assembly of solar-thermal panels or the photovoltaic modules; these panels can be connected either in parallel or series depending on the design objective. Solar panels are typically find use in residential, commercial, institutional, and light industrial applications.



Fig -8: Solar Plate

3.3 Photovoltaic (PV) Module

The photovoltaic cell is the basic structural unit of the photovoltaic module that generates current carriers when sunlight falls on it. The power generated by this photovoltaic cell is very small. To increase the output power the photovoltaic cells are connected in series or parallel to form photovoltaic module. The basic unit of a photovoltaic module is the solar cell, which consists of a p-n junction and that converts light energy directly into electrical energy. The Ipv is the light generated current, where Id is the diode current, Rash is the shunt resistance which describes the leakage current, Rs is the series resistance which describes the voltage drop as the charge carriers migrate from the p-n junction to the electrical contacts.



Fig -9: Electrical equivalent circuit of pv cell

3.3.1 Photovoltaic System

Photovoltaic modules generate by the DC current and the voltage. However, to feed the electricity to generate the grid, AC current and the voltage are needed. Inverter is the equipment which used to convert DC to AC. There are the different types of inverter configuration depending upon how the photovoltaic modules are connected to the inverter. The decision on what configuration should be used has to make for each case depending on the environmental and the financial requirements. If the modules are not use to identical or do not work under the same conditions, the MPP is different in each of the panel and the resulting voltage power characteristic has their multiple maxima, which constitutes a problem, because most MPPT algorithms converge to the local maximum which depending on the starting point.

3.3.2 Photovoltaic cell

PV cells are made of semiconductor materials, such call as silicon. For solar cells, thin semiconductor water is specially treated to form an electric field that is positive on one side and negative on the other. When the light energy strikes the solar cell and electrons are knocked loose from the atoms in the semiconductor material. If the electrical conductors are attached to the positive and the negative sides, forming the electrical circuit, the electrons can be captured in the form of



Fig -10: Photovoltaic cell

an electric current that is, electricity. This electricity can then be used to power a load. A photovoltaic cell can either be a circular or square in construction.

3.3.3 Pulse Width Modulation

Pulse Width Modulation refers to the method which is carrying information on a train of pulses, the information being encoded in the width of the pulses. The pulses have constant amplitude but their duration varies in the direct proportion to the amplitude of analog signal. The output voltage control is easier with PWM than other schemes and can be achieved without any additional components. The lower order harmonics are either minimized or eliminated all together. The filtering requirements are minimized as the lower order harmonics are use to eliminated and higher order harmonics are filtered easily.

3.3.4 Single Phase PWM Inverter

In many industrial applications, to control the output voltage of the inverters is necessary for the following reasons

1. To adjust with variations of dc input voltage

2. To regulate voltage of inverters

3. To satisfy the contain volts and frequency control requirement There are various techniques to vary the inverter gain.

4. RELATED WORK

In this paper, a digital logic control circuit is proposed for the solar fed CMLI which use to achieve higher levels which reduced number of switches without the requirement of bidirectional switches, filter components, detailed look-up that the tables and output transformers. The techniques include binary, trinary and modified multilevel connections to achieve 15 levels and 31 levels, respectively. The four stages CMLI power circuit used to achieve the 15 levels by the binary mode using digital switching technique comprises of the counters and the logic functions. The same power circuit is used to achieve the 27 levels by using the trinary mode. In modified multilevel connection, a 31 level is achieved with the single stage inverter by addition of input voltages using the embedded controller based on the proposed switching sequence. The comparison of the three methods with the existing techniques are analyzed and the experimentally verified.

4.1 Block Diagram



Fig -11: Block diagram of proposed system

4.1.1Block Diagram explanation

The modular multilevel converter (MMC) is a new type of voltage source converter (VSC) for a medium or a highvoltage dc power transmission and also other applications like connecting renewable resources to main grids. The main aim of the modular multilevel converter is to improve the power quality of solar powered system by reducing the THDs and as well as increasing the voltage level.

The structure of this topology is based on the several modules in which each of the modules consists of a floating capacitor, a diode and a switch. A solar photovoltaic panel is modeled by the interconnection of the solar cells in a series and a parallel to achieve the required rating.

The input supply is use to feed through photovoltaic module as a dc source and this output voltage magnitude is boost to require the level to obtain by the line voltage is employed. The output of the DC power from the photovoltaic panel supplied to the modular for multilevel DC/AC power converter.



A Pulse Generator generates the control signals or firing the pulse for the inverter to maintain the level of output. Some applications are: industrial machines, automobiles medical equipment, household appliances, airplanes, high power applications.

4.2 Simulink - Circuit diagram

In this circuit diagram, the modification is made in the both control circuit and the predominately in power circuit to obtain the 31 levels with only eight switches. The addition of diode and the capacitor (1nF) is to normalize the output within the given interval. Let the photovoltaic array inputs be V1–V4. During the level 1, V1 alone is given as input to the inverter and V2, V3, V4 in OFF condition. Similarly the 31 level is achieved by the controlling the ON/OFF status of the input voltages in Simulink. This has some of the advantages: Reduce the switching loss, High efficiency, Reduced THD, Reduced number of switches.



Fig -12: Simulink of proposed circuit diagram

4.2.1 Simulation explation

The proposed Photovoltaic Power using the Modular Multilevel Converter (MMC) is a simulated with MATLAB/SIMULINK Software. The photovoltaic array for the experimental setup is composed of numbers of cells connected in the series to form a module and to generate the voltage of 48V, 96V, 192V and 384V. The PWM Generator is used for providing the gating signals to the modular selection and the inverters. The simulation results show that this system is able to adapt the Photovoltaic Power using the Modular Multilevel Converter (MMC) for the fast response and good transient performances. In addition, the result of the simulation use to shows the increased efficiency of the Photovoltaic Power using Modular Multilevel Converter (MMC) because of the reducing the switching losses in the system. This system can provide high efficiency and also the low switching losses.



Fig -13: Proposed simulation



Fig -14: Switching sequence of 31 level

4.2.2 Simulation Output



Fig -15: Proposed simulation output

4.3 FFT analysis of simulation output



Fig -16: FFT analysis of output

5. CONCLUSION

A 31 level is obtained by MMC by adding one more switch and one more dc sources in solar fed modular multilevel inverter for the elimination of certain harmonic orders, reduced switching losses; reduced number of switches are developed and compared with 15 levels MMC for the power quality improvement. The advantages of this method include simple computational algorithm gives high efficiency, reduced total harmonic distortion and minimum requirement of filters. Moreover, this technique can be implemented in both standalone and grid interacted PV systems.

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