

Mitigation of Voltage and Current Harmonics Using UPQC Base On Synchronous Reference Frame Method

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ABSTRACT: This paper presents a three -phase active power filter for harmonic compensation which is consists of a series active power filter and a shunt passive filter. The active filter connected in series to a source acts as a harmonic isolator between the sources and loads whereas the shunt passive filter is connected in parallel with a load and suppresses the harmonic current produced by the load. Matlab /Simulink and are used for simulation. From the simulation results it is confirmed that the filter topology is capable of compensating the load current and voltage harmonic distortion within the stipulated limits laid down by the IEEE 519 standard.

KEYWORDS: Shunt active power filter, series active power filter, voltage harmonics, current harmonics, UPQC.

I. INTRODUCTION

Power-electronics circuits are widely used in industrial equipment, such as frequency changers, motor-drive systems (AC voltage controller, chopper), etc. Such equipment presents nonlinear impedance to the utility, generating large harmonic currents and voltages with wellknown adverse effects, such as low power factor, low efficiency and destruction of other equipment. Also, some precision instruments and communication equipment will be interfered with the EMI.

The effects of current harmonic distortion are poor utilization of distribution wiring and plant, increased power loss, high current flow in the neutral line and dangerous cable overheating. Voltage harmonic interrupts the proper operation of digital electronics mainly communications and process control, which needs sinusoidal supply voltage. Harmonic problem may result in mal-operation of protection equipment, which does not; itself draw harmonic currents from the supply.

Active filters, different from the passive filter, have the capability of dynamically adjusting to the conditions of the system in terms of harmonics and reactive power compensation. The effectiveness of Active Power Filter depends on the requirements, inverter type and topology. On the basis of topology Active Power Filter can be classified as series connected Active Power Filter and Shunt connected Active Power Filter. Series connected Active Power Filter can be connect before the load in series with the mains using a matching transformer to eliminate the voltage harmonics and to balance and regulate the terminal voltage of the load. Also it can be used to regulate the negative sequence voltage at the load. So the series Active Power Filter works as a controllable voltage source. The drawbacks of this series connected Active Power Filter is, it only compensates the voltage harmonics and another problem is for the short circuit in the load end. This short circuit current passes through the series transformer winding, which may overload the series transformer.

II. SYSTEM CONFIGURATION

The proposed topology consists of a series and shunt active power filter. The active filter connected in series to a source acts as a harmonic isolator between the source and load whereas the shunt active filter is connected in parallel with a load and suppresses the harmonic current produced by the load as shown in Figure 1.



Figure 1 system configuration

The series active filter compensates the harmonic voltage by synchronous reference frame (SRF) -based controllers. The

shunt active filter compensates the harmonic current by using synchronous reference frame (SRF) -based controllers. The three-Phase PLL used for generation of the unit vector coordinates ($\sin\theta$ and $\cos\theta$) used in the SRF-based algorithms. The SRF-based controller was used to generate the sinusoidal compensating current and voltage references applied to a three-phase line-interactive UPS System.

III.CURRENT AND VOLTAGE HARMONIC DETECTION

In this paper this synchronous reference frame (SRF) method is used to generate the reference voltage and current required to compensate the voltage and current harmonics. The three voltage and/or current signals in a-b-c coordinates are transformed to the orthogonal α - β stationary frame using (1) and then to synchronous rotating frame (d-q) using (2). Based on the control needs the required components are extracted/ separated in d-q frame. To generate the reference signals and thus, to transform them back to original frame, the inverse transformation from d-q to α - β frame, and then to a-b-c frameis carried out utilizing (3) and (4), respectively.

i. Clark's Transformation

It transforms sensed source voltage /current signal from a-b-c stationary to α - β stationary coordinate system by following equation

$$\begin{bmatrix} x_{\alpha} \\ x_{\beta} \\ x_{o} \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \\ \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} \end{bmatrix} \begin{bmatrix} x_{ca} \\ x_{cb} \\ x_{cc} \end{bmatrix}$$
(1)

ii. Park's Transformation

Now this signal $\alpha\text{-}\beta$ is converted in d-q frame by using equation

$$\begin{bmatrix} x_d \\ x_q \end{bmatrix} = \begin{bmatrix} \cos \omega t & \sin \omega t \\ -\sin \omega t & \cos \omega t \end{bmatrix} \begin{bmatrix} x_\alpha \\ x_\beta \end{bmatrix}$$
(2)

iii. Reverse Park's Transformation (α - β to d-q)

$$\begin{bmatrix} x_{\alpha} \\ x_{\beta} \end{bmatrix} = \begin{bmatrix} \cos \omega t & -\sin \omega t \\ \sin \omega t & \cos \omega t \end{bmatrix} \begin{bmatrix} x_{d} \\ x_{q} \end{bmatrix}$$
(3)

iv. Reverse Clark's Transformation (d-q to a-b-c)

$$\begin{bmatrix} x_a \\ x_b \\ x_c \end{bmatrix} = \sqrt{\frac{2}{3}} \cdot \begin{bmatrix} 1 & 0 & \frac{1}{\sqrt{2}} \\ -\frac{1}{2} & \frac{\sqrt{3}}{2} & \frac{1}{\sqrt{2}} \\ -\frac{1}{2} & -\frac{\sqrt{3}}{2} & \frac{1}{\sqrt{2}} \end{bmatrix} \begin{bmatrix} x_a \\ x_\beta \\ x_o \end{bmatrix}$$

Where x represents the variable under consideration, and can be voltage or current.

A.Current SRF controller

In this method the measured load currents are transformed into the rotating reference frame (d-q frame) that is synchronously rotating at the line voltage frequency using (1) and (2). The line frequency components of the load currents become DC quantities and the harmonic components are frequency shifted by ωt in the d-q reference frame. A high pass filter in the d-q frame, with a cut-off at the line frequency can be used to extract the DC components. Currents to transform them back to original frame, the inverse transformation from d-q to α - β frame, and then to a-b-c frame is carried out utilizing (3) and (4). Reference compensating currents ($i_{ca^*}, i_{cb^*}, i_{cc^*}$) generated using current SRF controller which shown in figure 2

B. Voltage SRF controller

In this method the measured voltages are transformed into the rotating reference frame (d-q frame) that is synchronously rotating at the line voltage frequency using (1) and (2).The line frequency components of the voltages become DC quantities and the harmonic components are frequency shifted by ω s in the d-q reference frame. A high pass filter in the d-q frame, with a cut off at the line frequency can be used to extract the DC components. Voltages to transform them back to original frame, the inverse transformation from d-q to α - β frame, and then to ab-c frame is carried out utilizing (3) and (4).Reference compensating voltages($v_{ca*}, v_{cb*}, v_{cc*}$) generated using voltage SRF controller which shown in figure2

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Figure 2 current and voltage harmonic detection

IV.HYSTERESIS CURRENT CONTROLLER

Hysteresis current control method of generating the switching signal for the inverter switches in order to control the inverter output current [5]. It is adopted in active filter due to best among other current control methods, easy implementation and quick current controllability.[4] It is basically a fed back current control method, where the actual current continuously tracks the reference current in the hysteresis band(Figure 4). The actual current or voltage is compared with respect to hysteresis band which decides switching pulse of voltage source inverter.



Figure 3. Hysteresis Controller

As the current crosses a set hysteresis band, the upper switch in the half-bridge is turned off and the lower switch is turned on (Figure 5). As the current exceeds the lower band limit, the upper switch is turned on and the lowers witch is turned off. The switching frequency depends on how fast the current changes from upper limit to lower limit and vice versa. This, in turn depends on voltage v_d and load inductance.



Figure 5. Hysteresis Band and Generation of Pulses

V.SIMULATION RESULTS

The proposed model for a shunt series active power filter using harmonic detection method with hysteresis current controller has been successfully modelled and tested using MATLAB/SIMULINK toolbox.



Figure 4. The proposed model for a shunt series active power filter



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Figure 6.Distorted current at source without compensation



Figure 7. FFT analysis for above distorted current wave forms

Figure 8 shows FFT analysis for figure 7 wave from and distorted current THD is 21.7%.



Figure 8.Distorted voltages at source without compensation



Figure 9. FFT analysis for above distorted voltages wave form distorted voltage THD is 7.9 %.



Figure 10.Current at source after compensation



Figure 11. FFT analysis for above currents wave form and distorted current THD is 1.89%.



Figure 12.voltages at source after compensation



Figure 13. FFT analysis for above voltages wave form distorted current THD is 3.4 %.



Results

Parameters	Without compensation	With compensation
voltage		
(THD-V) %	7.9	3.4
current		
(THD-I) %	21.7	1.89

VI. CONCLUSION

This project presents some insight about the active power flow in an electrical system with a three Phase Series and shunt Active power filter. The presented three -Phase Series and shunt Active power filter makes use of two back-to-back converters with different purposes and distinct coupling with the electrical power system. One is shunt connected to the electrical grid through a transformer, and the other is series connected without a transformer to the electrical grid. The Shunt Converter performs the regulation of the DC link shared by both converters, by consuming or injecting sinusoidal current. The Series Converter compensates Power Quality issues related with the voltage waveform. The analysis of the active power flow through an electrical system is performed mathematically, and Simulation results were obtained to verify those active power flow issues in a harmonic free electrical System.

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