

# Comparative Analysis of 13-level Asymmetrical Multi-level Inverter Using Different Control Techniques

# Harshitha.N. G<sup>1</sup>, Dr Madhusudhana J<sup>2</sup>

<sup>1</sup>PG student [PE], Dept of Electrical Engineering, UVCE, Bangalore,Karnataka, India <sup>2</sup>Associate Professor, Dept of Electrical Engineering, UVCE, Bangalore,Karnataka, India

\*\*\*\_\_\_\_\_\_

**Abstract** – This paper presents an Asymmetrical Multi-level Inverter which generates stepped AC output voltage with the input DC voltage sources. The number of input DC sources used are less and are of lower magnitudes. The proposed Multi-level Inverter in Asymmetrical configuration has unequal DC sources produce 13-levels of output. The control techniques Equal Phase (EP) method, Half Equal Phase (HEP) method, Feed Forward (FF) method and Half Height (HH) method are used for the calculation of switching angles for the Multi-level Inverter. The inverter is simulated using all the four control techniques using MATLAB/ Simulink software. The performance of the inverter is analyzed and the comparison on Total Harmonic Distortion is presented.

*Keywords*: Multi-level Inverter (MLI), Asymmetrical Multilevel Inverter (AMLI), Equal Phase (EP) method, Half Equal Phase (HEP) method, Feed Forward (FF), Half Height (HH), Total Harmonic Distortion (THD).

## 1.INTRODUCTION (Size 11, cambria font)

Inverters are one among the types of converters used to convert input DC to AC output. Multi-level inverters are important tool of power converters used elaborately in high and medium power applications. Due to stepped output waveform, multi-level inverters produce lower harmonics and has better power quality of output voltage compared to two level and three level inverters.

A review on MLI topologies with reduced device count has been presented [1] which gives the understanding of the challenges that is been encountered in the newer topologies during the reduction of number of devices. Multi-level Inverter topologies like diode-clamped inverter (neutralpoint clamped), flying capacitor (capacitor clamped) inverter and Cascaded H-Bridge inverters have been reviewed and also relevant control techniques [2].

The three classical topologies of MLI are Neutral Point Clamped (NPC) [3], Flying Capacitor [4], Cascaded H-Bridge [5]. But NPC and FC topologies suffer from DC-link voltage balancing problem, bulk size of capacitor and requires a greater number of semiconductor components at higher levels [6]. The Asymmetrical Inverters overcome the drawback of using same value of DC sources [7]. An arrangement of semiconductor switches with mixed DC sources to obtain maximum voltage levels to improve the power quality [8]. The staircase output waveform of the MLI is obtained by fundamental frequency PWM schemes by calculating switching angles for each step and thereby minimizing THD of the MLI [9]. Switching angle calculation and percentage conduction of each switch are calculated using Equal Phase (EP) method, Half Equal Phase (HEP)method, Feed Forward (FF) method and Half Height (HH) method by non-linear equations. The optimum switching angles are calculated by each method [11] to validate the results.

In this paper, a 13-level Asymmetrical MLI is considered a comparative study on the performance using different control techniques is presented. The simulation results obtained are studied and conclusion are drawn.

#### **1.1 PRINCIPLE OF OPERATION OF PROPOSED CIRCUIT**

This topology uses eight voltage sources in which the voltage sources in each module are in the ratio 1:2. By having unequal DC sources, the MLI produces different output voltage levels by fewer semiconductors and lower harmonic components as well.



#### Fig 1. Proposed Topology

Fig 1 represents the proposed topology with 14 switches (8 unidirectional switches and 3 bidirectional switches), 14 diodes and 4 unequal DC sources. This configuration generates 6 positive levels, 6 negative levels and zero level (totally 13 levels). The module can be connected Fig 1 Proposed MLI Topology in series as cascade connection easily to obtain higher levels. The switching pulses are selected in such a way that there is less stress on switches. Table1 shows the switching pattern of the inverter.



**JET** Volume: 07 Issue: 12 | Dec 2020

www.irjet.net

TABLE I Switching Table of 13-level inverter											
	<b>S</b> <sub>1</sub>	S <sub>2</sub>	S <sub>3</sub>	S4	<b>S</b> 5	<b>S</b> <sub>6</sub>	S7	S8	S9	S10	S11
0v	0	0	0	0	0	0	1	1	1	0	0
1v	1	0	0	0	0	0	0	1	1	0	0
2v	0	0	0	0	1	0	1	1	0	0	0
3v	1	0	0	0	1	0	0	1	0	0	0
4v	1	0	0	1	0	0	0	0	1	1	1
5v	0	0	0	1	1	0	1	0	0	1	1
6v	1	0	0	1	1	0	0	0	0	1	1
-1v	0	0	1	0	1	0	1	0	0	0	0
-2v	1	0	1	0	0	0	0	0	1	0	0
-3v	0	0	1	0	0	0	1	0	1	0	0
-4v	0	0	1	0	0	1	1	0	0	0	1
-5v	0	1	1	0	0	0	0	0	1	1	0
-6v	0	1	1	0	0	1	0	0	0	1	1

#### **TABLE 1** Switching Table of 13-level Inverter

# 2. CONTROL TECHNIQUES FOR MULTILEVEL INVERTER

Switching angle is the moment of the voltage level change at the output. For an

m-level waveform there are 2(m-1) switching angles are needed. We call them as  $\alpha 1, \alpha 2, \alpha 3.....\alpha m-2, \alpha m-1$ . Since the sine wave is a symmetrical waveform, the negative half cycle is centrally symmetrical to its positive half cycle; and the waveform of the second quarter period is mirror symmetrical to the waveform of its first quarter period. So, we call the switching angles in the first quadrant period i.e., 0°-90 ° as main switching angles. Main Switching Angles in the first quarter of the sine wave (i.e., 0° to 90°):

 $\alpha 1, \alpha 2, \alpha 3$ .....,  $\alpha (m-1)/2$  .....(1) The switching angles in the second quarter of the sine wave

(i.e.,  $90^{\circ}$  to  $180^{\circ}$ ) are:  $\alpha(m+1)/2=\Pi-\alpha(m-1)/2, \Pi-\alpha(m-2)/2 \dots \Pi-\alpha 1$ .....(2) The switching angles in third quadrant of the sine wave (i.e., $180^{\circ}$  to  $270^{\circ}$ ) are  $\alpha m=\Pi+\alpha 1$ ....,  $\Pi+\alpha(m-1)/2$ ......(3) The switching angles in the fourth quadrant (i.e., $270^{\circ}$  to  $360^{\circ}$ ) are

The main switching angles of the proposed inverter are determined from the following methods.

#### 2.1Equal Phase (EP) Method:

In the equal phase method, the switching angles are

distributed averagely in the range  $0^{-\pi}$ . The main switching angles are obtained by the formula given below:

The main switching angles, by EP method, of the proposed 13-level inverter are determined from the equation (5)  $\alpha 1=13.84^{\circ}, \alpha 2=27.69^{\circ}, \alpha 3=41.53^{\circ}, \alpha 4=55.38^{\circ}, \alpha 5=69.23^{\circ}$ 

The other switching angles in second, third and fourth quadrants of sine wave are derived from the main switching angles according to the equations (2), (3) and (4). Table 2 shows the total switching angles required to get the sinusoidal wave shape by EP method.

Table 2 Switching Angles of Equal Phase Metho	d
---	---

	Switching		Switching
	Angles		Angles
α1	13.84	α14	193.84
α2	27.69	α15	207.69
α3	41.53	α16	221.53
α4	55.38	α17	235.38
α5	69.23	α18	249.23
α6	83.07	α19	263.07
α7	96.92	α20	276.92
α8	110.76	α21	290.76
α9	124.61	α22	304.61
α10	138.46	α23	318.46
α11	152.30	α24	332.30
α12	166.15	α25	346.15
α13	180	α26	360

#### 2.2 Half Equal Phase (HEP) Method:

The waveform obtained from the EP method looks like a triangle waveform, so to get some better output waveform, another method called Half Equal Phase Method (HEPM) is established, by this approach we can get better and reduced harmonic output waveform. The main switching angles are in

the range 0°-90°, which are obtained by the formula given  $\alpha i = \frac{180*i}{2}$ 

below. N+1 where i=1,2...... (m-1)/2.....(6)

For the proposed 13-level inverter the main switching angles, by HEP method, are calculated from the equation (6).  $\alpha 1=12.85^{\circ}$ ,  $\alpha 2=25.71^{\circ}$ ,  $\alpha 3=38.57^{\circ}$ ,  $\alpha 4=51.42^{\circ}$ ,  $\alpha 5=64.28^{\circ}$  After that the other switching angles are derived from the above main switching angles, according to the equations (2), (3) and (4). And are tabulated in Table 3.

**Table 3** Switching Angles of Half Equal Phase Method

			1
	Switching		Switching
	Angles		Angles
α1	12.85	$\alpha_{14}$	192.85
$\alpha_2$	25.71	$\alpha_{15}$	205.71
α3	38.57	$\alpha_{16}$	218.57
$\alpha_4$	51.42	<b>α</b> <sub>17</sub>	231.42
α <sub>5</sub>	64.28	$\alpha_{18}$	244.28
α6	77.14	α19	257.14
α7	102.85	α20	282.85
α8	115.71	<b>α</b> 21	295.71
α9	128.57	α22	308.57
α10	141.42	α23	321.42
<b>α</b> 11	154.28	α24	334.28
<b>α</b> 12	167.14	α25	347.14
α13	180	α26	360

#### 2.3 Feed Forward (FF) Method:

In the above three methods, we can observe that there are wider gaps between the positive half-cycle and negative halfcycle. In order to reduce the gaps in between the positive half cycle and negative half-cycle, another approach called the Feed Forward Method (FFM) was established to find the main switching angles. The main switching angles are determined by the following formula.

$$\alpha i = \frac{1}{2} \sin^{-1} \frac{2i-1}{N-1}$$

where i=1,2..... (m-1)/2..... (7)

The main switching angles, by FF method, of the proposed 13-level inverter are  $\alpha 1=2.39^{\circ}$ ,  $\alpha 2=7.23^{\circ}$ ,  $\alpha 3=12.31^{\circ}$ ,  $\alpha 4=17.84^{\circ}$ ,  $\alpha 5=24.29^{\circ}$  After that **the** other switching

angles are derived from the main switching angles, by using equations (2), (3) and (4). Table4 shows the total switching angles, of the proposed inverter, by FF method. **Table 4** Switching Angles of Feed Forward Method

	Switching		Switching
	Angles		Angles
α1	2.39	α14	182.39
α2	7.23	<b>α</b> 15	187.23
α3	12.31	α16	192.31
α4	17.84	α17	197.84
α5	24.29	α18	204.29
α <sub>6</sub>	33.22	α19	213.22
α7	146.77	α20	326.77
α8	155.70	α21	335.70
α9	162.15	α22	342.15
α10	167.68	α23	347.68
α <sub>11</sub>	172.76	α <sub>24</sub>	352.76
α <sub>12</sub>	177.60	α25	357.60
α <sub>13</sub>	182.39	α <sub>26</sub>	362.39

## 2.4 Half Height (HH) Method:

In the above two methods the switching angles are arranged in a simple manner, but the waveform at the output is not a sine wave shape. According to the sine function a new method called Half Height Method (HEM) was established to determine new switching angles. The main switching angles are obtained from the following formula.

$$\alpha i = \sin^{-1} \frac{2i-1}{N-1}$$

where i=1, 2..... (m-1)/2..... (8)

In this method the main switching angles of the proposed 13level inverter are  $\alpha 1=4.78^{\circ}$ ,  $\alpha 2=14.47^{\circ}$   $\alpha 3=24.62^{\circ}$ ,  $\alpha 4=35.68^{\circ}$ ,  $\alpha 5=48$ . 59°.The other switching angles are derived from the main switching angles according to the equations (2), (3) and (4). Table5 shows the total switching angles, by HH method, needed to get the sinusoidal wave shape.



	Switching		Switching
	Angles		Angles
$\alpha_1$	4.78	α <sub>14</sub>	184.78
α2	14.47	<b>α</b> 15	194.47
α3	24.62	α <sub>16</sub>	204.62
α4	35.68	α17	215.68
α5	48.59	<b>α</b> 18	228.59
α6	66.44	α19	246.44
α7	113.55	α20	293.55
α8	131.40	α <sub>21</sub>	311.40
α9	144.31	α22	324.31
<b>α</b> 10	155.37	α23	335.37
α11	165.52	α24	345.52
<b>α</b> 12	175.21	α25	355.21
α13	184.78	α26	364.78

# 4 SIMULATION OF PROPOSED CIRCUIT & DICUSSION OF RESULTS

Fig 2 shows the Simulink model of 13-level multi-level inverter. Fig 3 shows the switching pulses given to the inverter. To check the performance of proposed 13-level Asymmetrical MLI to generate 13 output voltage levels. The circuit is simulated through MATLAB Simulink software. The magnitude of DC sources used in the simulation circuit are 1VDC and 2VDC to which the inverter generates the output voltage of 13 levels. The switches used are IGBT which operates at fundamental switching frequency of 50Hz. Table 6 depicts the specifications considered for the simulation of the inverter.

TABLE 6 Specifications	of 13-level Inverter
------------------------	----------------------

V1 = V3 = 60V
V2 = V4 = 120V
R load; R= 25.45 Ω
RL load; R= 20.36 Ω, L= 48.6mH

Т



Fig 2 Simulation model of proposed topology



#### Fig3 Switching pulses

Fig 4 shows the output voltage and current waveform for R and RL load using Equal Phase Control technique. Fig 5 shows the FFT analysis of the 13-level inverter for R and RL load using Equal Phase Control technique.







(ii) Fig 4 Output voltage and current waveform of Equal Phase Method(i) R load (ii) RL load





Fig 5 FFT analysis of Equal Phase Method (i) R load (ii) RL load

Fig 6 shows the output voltage and current waveform for R and RL load using Half Equal Phase Control technique. Fig 7 shows the FFT analysis of the 13-level inverter for R and RL load using Half Equal Phase Control technique.





Fig 6 Output voltage and current waveform of Half Equal Phase Method (i) R load (ii) RL load



Fig 8 shows the output voltage and current waveform for R and RL load using Feed Forward Control technique. Fig 9 shows the FFT analysis of the 13-level inverter for R and RL load using Feed Forward Control Technique.



International Research Journal of Engineering and Technology (IRJET)e-ISSN: 2395-0056Volume: 07 Issue: 12 | Dec 2020www.irjet.netp-ISSN: 2395-0072









Fig 8 Output voltage and current waveform of Feed Forward Method (i) R load (ii) RL load



Fig 9 FFT analysis of Feed Forward Method (i) R load (ii) RL load

Fig 10 shows the output voltage and current waveform for R and RL load using Half Height Control technique. Fig 11 shows the FFT analysis of the 13-level inverter for R and RL load using Half Height Control Technique. Table 6 shows the comparison between control techniques with respect to Total Harmonic Distortion.







Fig 10 Output voltage and current waveform of Half Height Method (i) R load (ii) RL load



(ii) Fig 11 FFT analysis of Half Height Method (i) R load (ii) RL load

Table 6 Comparison of control techniques				
	THD in %			
Control Technique/ Load	R load	RL load		
Equal Phase Method	31.77%	31.87%		
Half Equal Phase Method	31.45%	30.02%		
Feed Forward Method	30.8%	31.42%		
Half Height Method	6.39%	6.93%		

## **3. CONCLUSION**

In this paper a comparative study on performance of 13level Asymmetrical MLI using different control techniques is carried out. The simulation for all the techniques is done using MATLAB/ Simulink and the results obtained are presented. The simulation results show that the THD obtained for load R=20.36  $\Omega$  & L=48.6mH is 31.77% for Equal Phase Angle technique, whereas for Half Height is 6.37%. Further studies can be conducted by incorporating soft computing techniques for better results.

#### REFERENCES

[1] Gupta, K.K.; Ranjan, A.; Bhatnagar, P.; Sahu, L.K.; Jain, S., "Multilevel Inverter Topologies With Reduced Device Count: A Review," in Power Electronics, IEEE Transactions on, vol.31, no.1, pp.135-151, Jan. 2016.

[2] Jose Rodriguez, Jih-Sheng Lai, Fang Zheng Peng, "Multilevel Inverters: A Survey of Topologies, controls and Applications" IEEE Transactions on Industrial Electronics vol. 49, No. 4, Aug 2002.

[3] A. Nabae, I. Takahashi & H.Akagi," A new neutral-pointclamped PWM inverter," IEEE Trans. Ind. Appl., vol. IA-17, no. 5, pp. 518–523, Sep./Oct. 1981.

[4] T. A. Meynard and H. Foch, "Multi-level choppers for high voltage appli- cations, in Proc. Eur. Conf. Power Electron. Appl" 1992, vol.2, pp.45–50.

[5] F.Z. Peng,J.-S.Lai, J.W.McKeever, and J.Van Coevering, "Amultilevel voltage source inverter with separate DC sources for static var generation," IEEE Trans. Ind. Appl., vol. 32, no. 5, pp. 1130–1138, Sep./Oct. 1996.

[6] E.Samadaei, M. Kaviani, K. Bertilsson," A 13-level Module (K-type) with two Dc sources for Multi-level Inverter," IEEE Transactions on Industrial Electronics 2018.

[7] E. Samadaei, S. A. Gholamian, A. Sheikholeslami and J. Adabi, "An Envelope Type (E-Type) Module: Asymmetric Multilevel Inverters With Reduced Components," in IEEE Transactions on Industrial Electronics, vol. 63, no. 11, pp. 7148-7156, Nov. 2016

[8] E. Samadaei, A. Sheikholeslami, S. A. Gholamian and J.Adabi, "A Square T-Type (ST-Type) Module for Asymmetrical Multilevel Inverters," in IEEE Transactions on Power Electronics, vol. 33, no. 2, pp. 987-996, Feb. 2018.

[9] R.Ramaprabha, S. Malathy & Ramya Krishnan N, "A 17-Level Asymmetric Multilevel Inverter with Fundamental Frequency PWM Schemes", in IEEE Transactions on Power Electronics, 2019.

[10] N. Jalakanuru, M. Kiber, "Switching Angle Calculation by EP, HEP, HH and FF Methods For Modified 11-Level Cascade H-Bridge Multilevel Inverter," International Journal of Engineering Science Invention (IJESI), vol. 6, no. 12, pp. 69-75, 2017.

[11] Madhusudhana J, Rakshitha R Prabhu, P S Puttaswamy, 'Comparative Analysis of 31-level Asymmetrical Inverter Using Different Control Techniques', International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol. 8, Issue 5, May 2019

[12] Madhusudhana J, P S Puttaswamy, Harshit Agrawal "A comparative analysis of different multilevel inverters" International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering (IJAREEIE) Vol. 5, Issue 7, July 2016