

DESIGN AND ANALYSIS OF MICROSTRIP PATCH ANTENNA SUITABLE FOR Ku BAND SATELLITE COMMUNICATION

Mr.S.Dhamodharan, K.Shanmugapriya², S.Sindhuja³, S.Sruthi⁴, I. Stephypraba⁵

¹ Assistant Professor, Department of ECE, SSM Institute of Engineering and Technology, Dindigul, Tamilnadu-624002

^{2,3,4}Department of ECE, SSM Institute of Engineering and Technology, Dindigul, Tamilnadu-624002

Abstract - This paper represents the well formed rectangular shape truncated micro strip patch antenna. The proposed antenna is simulated using High-Frequency Structure Simulator (HFSS) tool. The purpose of this paper is to design the rectangular micro strip patch antenna and concentrating on the effect of antennas radiation pattern and gain. By considering the parameters of relative dielectric constant (ϵ_r), substrate material and thickness, length & width of the patch. Conducting patch is formed by using rectangular configuration. Microstrip patch antennas are low cost, small size and easy to fabricate and they play a dominant role in wireless communication. The antenna is designed at Ku band frequency (17.5 GHz) with multiple slots on the rectangular patch. These slots on the patch shift the resonant frequency to the lower side and also affect various antenna parameters. The antenna is designed by using RT duroid material that has relative permittivity of 2.2 and loss tangent 0.0009.

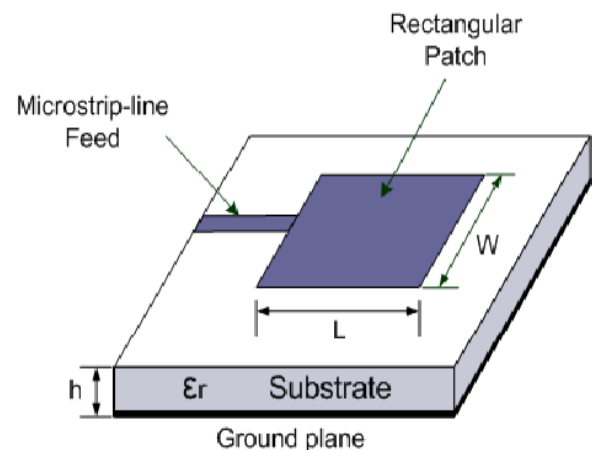


Fig.1. Microstrip Patch Antenna

Key Words: HFSS, Microstrip, RT duroid, Relative permittivity, Satellite communication.

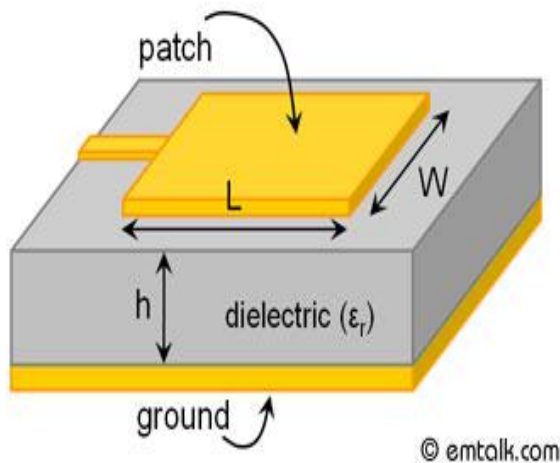
1. INTRODUCTION

1.1 ANTENNA

Antenna is an electrical device which converts electrical power into radio waves and vice versa without an efficient antenna, EMF (electro magnetic energy would not be radiated and wireless communication over the long distance would be impossible. Antennas can be designed to transmit and receive radio waves in all horizontal directions equally (omnidirectional antenna), or preferentially in a particular direction called directional or high gain antennas. Radio waves are electromagnetic waves which carry signals through the space or air at the speed of light with almost no transmission loss.

1.2 MICROSTRIP PATCH ANTENNA

A microstrip antenna (also known as printed antenna) usually means an antenna fabricated using microstrip techniques on printed circuit board (PCB). It is a kind of Internal Antenna. They are mostly used at microwave frequencies. The most common type of microstrip antenna is the patch antenna. Antennas using patches as constitutive elements in an array are also possible. A patch antenna is a narrowband, wide-beam antenna fabricated by etching the antenna element pattern in metal trace bonded to an insulating dielectric substrate, such as a printed circuit board, with a continuous metal layer bonded to the opposite side of the substrate which forms a ground plane. Common microstrip antenna shapes are square, rectangular, circular and elliptical, but any continuous shape is possible. Some patch antennas do not use a dielectric substrate and instead are made of a metal patch mounted above a ground plane using dielectric spacers; the resulting structure is less rugged but has a wider bandwidth. Because such antennas have a very low profile, are mechanically rugged and can be shaped to conform to the curving skin of a vehicle, they are often mounted on the exterior of aircraft and spacecraft, or are incorporated into mobile radio communications devices. It is used as telecommunication.



The most commonly employed microstrip antenna is a rectangular patch which looks like a truncated microstrip transmission line. It is approximately of one-half wavelength long. When air is used as the dielectric substrate, the length of the rectangular microstrip antenna is approximately one-half of a free-space wavelength. As the antenna is loaded with a dielectric as its substrate, the length of the antenna decreases as the relative dielectric constant of the substrate increases. The resonant length of the antenna is slightly shorter because of the extended electric "fringing fields" which increase the electrical length of the antenna slightly. An early model of the microstrip antenna is a section of microstrip transmission line with equivalent loads on either end to represent the radiation loss. The advantages of Microstrip Antennas like Light weight, Inxpensive and robust, Easy to fabricate (use etching and photolithography), Easy to feed (coaxial cable, microstrip line, etc.) Easy to use in an array or incorporate with other microstrip circuit elements, Dual frequency antennas can be made easily, Compatible with MMIC (Microwave monolithic Integrated circuit design.), Feed lines and matching networks are fabricated simultaneously with the antenna structure. make them Suitable for numerous applications [9].

2. EXISTING SYSTEM

Since decades microstrip antennas are being developed and day to day their application is also being raised continuously [1-2]. The growth of this microstrip technology has a vast rise in the field of wireless communication. It is printed on RT duroid substrate having relative permittivity 2.2, loss tangent 0.0009 and thickness of $h = 1.6\text{mm}$. Patch dimensions are calculated from the equations (1) and (2), ground plane dimensions are calculated from the equations (6) and (7), and the length of feed line from the equation (9). The feed given is inset feed and the input given has an input impedance of 50 ohms. The rectangular patch has four slots on it which are equally spaced. The dimensions of the proposed antenna are listed in table 1. All the slots are given same dimensions with length 2mm and width 0.5mm, and they are placed parallel to each other and symmetric to the patch. The proposed antenna is designed to work at 15 GHz.

Slots are designed on the radiating patch to improve the antenna performance. The wavelength for the proposed design is 20mm, and the size of the antenna is given less than half of the wavelength for better radiation efficiency. The simulated structure is shown in figure 4 with two slots placed on top of the patch and remaining two at the bottom of the patch.

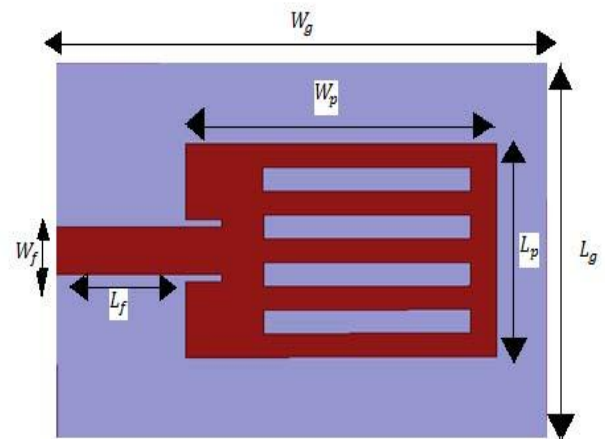


Fig.2. Geometry of proposed antenna with slots

2.1 Proposed antenna

The proposed antenna works at 15 GHz with multiple slots placed on the radiating patch. The proposed design includes two slots on the upper half and two slots on the lower half of the patch. Etching of slots affects various antenna performance characteristics like return loss, gain, bandwidth and shift the resonant frequency [8-9]. The proposed antenna is specified to work for satellite communication at 15 GHz.

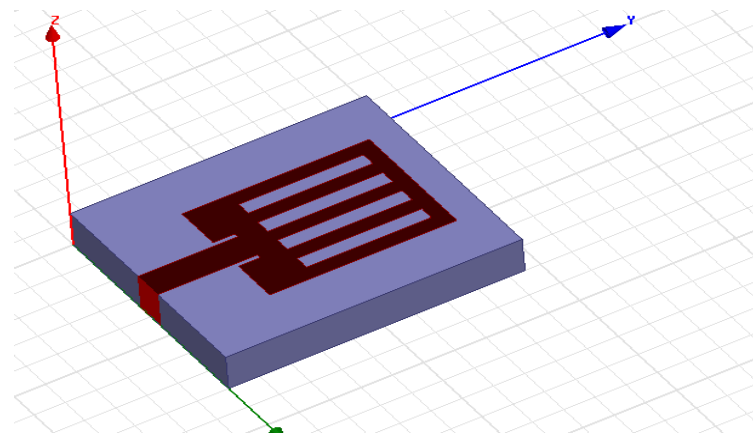


Fig. 3. Simulated structure

PARAMETERS	DIMENSIONS
Substrate length(Lg)	8mm
Substrate width(Wg)	9.5mm
Substrate thickness(h)	1.6mm
Patch length(Lp)	6mm
Patch width(Wp)	4.5mm
Feed line length(Lf)	3.2mm
Feed line width(Wf)	1mm

2.2 Software

HFSS

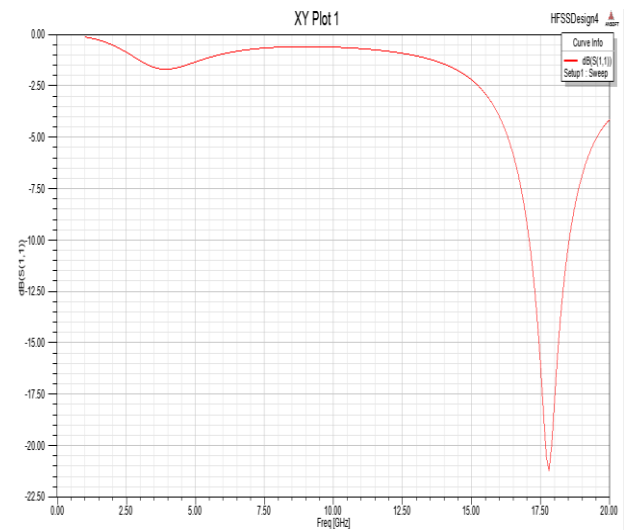
HFSS-Transient is a 3D full wave EM field solver based on the discontinuous Galerkin time domain method (DGTD). The finite element transient solver creates an unstructured mesh which incorporates HFSS adaptive meshing. Some typical applications include pulsed ground penetrating radar (GPR), electrostatic discharge, time domain reflectometry, transient field visualization and determining scattering centers for radar cross section (RCS). A plane electromagnetic wave impinges upon the aircraft at an oblique angle and various scattering interactions can be observed as a function of time and position.

2.3 High Performance Computing in HFSS

High performance computing (HPC) enables a range of different technologies in HFSS that allows efficient simulation of extremely large and complex problems. HPC leverages multiple cores through matrix multiprocessing, distributed frequency points (called spectral decomposition method or SDM), domain decomposition (DDM), parallel hybrid FEM/IE solving or the finite antenna array DDM. In addition hierarchical HPC solving is possible where frequency points can be distributed with each frequency point using multiple cores or machines for large scale DDM analysis at each frequency point, all in parallel.

3. Proposed work Result

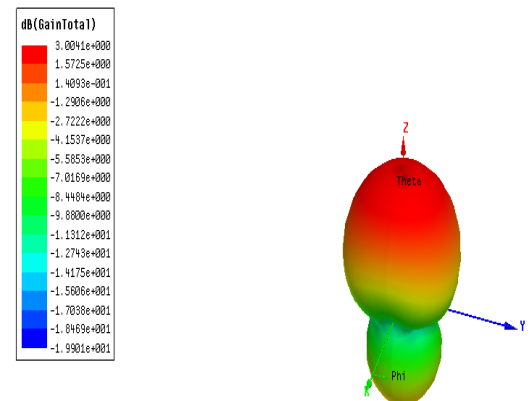
The proposed antenna is analyzed using HFSS software. The Return loss obtained for the simulated design is -21.2 db at 17.5GHz frequency. Since the obtained return loss is very minimum the reflection coefficient is relatively good and hence it can be concluded that a little power is reflected back. The return loss plot is against frequency is shown in fig(a).



Figure(a)Return Loss

3.1 Gain

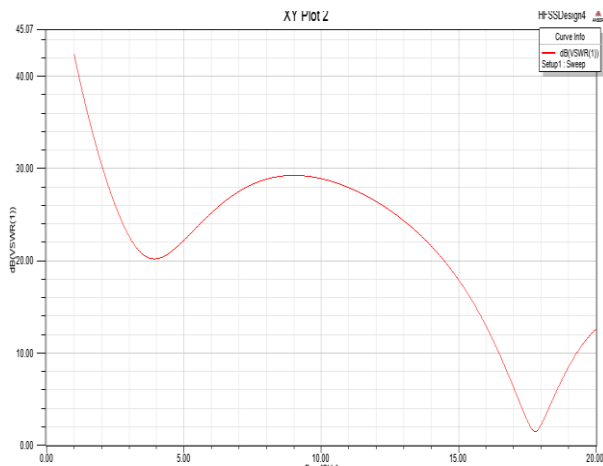
For the proposed antenna, the gain obtained is 3db. Since efficiency of the antenna is the ratio of gain it is observed the efficiency of antenna obtained is 95.62% the plot of gain is shown in fig(b)



figure(b)Gain

3.2 VSWR

VSWR is one way to find it and it is the ratio of maximum voltage and minimum voltage. When a system is perfectly matched the VSWR is equal to 1. From the fig(c) it is observed that the proposed design has VSWR is 1.06 hence it can be said that almost the feed and patch are perfectly matched.



figure(c) VSWR

4. CONCLUSION

A new structure based on the addition of slots which are equally spaced on the rectangular patch for satellite communication is proposed. The size of proposed design is 0.8cm length so, it is lightweight and flexible and moreover the statement that the better radiation efficiency can be achieved for the antenna having the size less the half wavelength is satisfied. It can be concluded that better bandwidth, return loss and VSWR can be achieved by etching slots on the radiating patch, and also these slots shifts the resonant frequency. For both the uplink and downlink process in satellite communications, this antenna can be used. The conception and simulation of rectangular microstrip patch antenna that operates in Ku frequencies was successfully designed using advanced design system. From observing the return loss, VSWR, it is very clear that this antenna works on the designed Ku frequency range. This research, detailed the designing of our Ku rectangular antenna in the Advanced Design System and High frequency structure simulator.

As the outlook work, we may extend our research to study a various slot antenna in affects the resonance frequency and the bandwidth.

REFERENCES

- [1] Pozar, and M. David, "Microstrip antennas", Proceedings of the IEEE, vol. 80(1), pp. 79- 91, Jan1992.
- [2] J.R. James, and P.S. Hall, Handbook of Microstrip Antennas, vol. 1.IEEE, Peter Peregrinus Ltd:Clarendon, 1989, pp. 1-17.
- [3] Raj Kumar, J. P. Shinde and M. D. Uplane, Effect of Slots in Ground Plane and Patch on Microstrip Antenna Performance, International Journal of Recent Trends in Engineering, vol, No.6, November 2009.
- [4] R. Garg, "Progress in Microstrip antennas", IETE technical review ,Vol. 18, No.2 & 3 pp. 85-98, March June 2001.

- [5] Souryendu Das, SunandanGokhroo ,Microstrip Patch Antenna at 7GHz for SatelliteCommunication International Journal of EngineeringTechnology Science and Research 0Volume2, Issue 11 November 2015.

- [6] M. Tanaka, and Jae-Hyeuk Jang, "Wearable microstrip antenna", Antennas and Propagaation Society International Symposium, vol. 2, pp.704-707, June 2003.

- [7] R. B. Waterhouse, S. D. Targibski and D. M. Koktoff,"Design andperformance of small printedantenna", IEEE Trans.,AP-46, 11, Nov.1998, P. 1629.

- [8] C. Kamtongdee, and N. Wongkasem, "A novel design of compcat 2.4GHz microstrip antennas",6th International Conference ECTI-CON, vol.2, pp. 766-769, May 2009.

- [9] B. Ghosh, S.M. Haque, and D. Mitra, "Minituarization of Slot AntennasUsing Slit and StripLoading", IEEE Transactions on Antennas andPropagation, vol. 59, pp. 3922-3927, Aug 2011.

- [10] D.M Pozar, Microwave engineering, John wiley& sons, 2009.

- [11] R.JothiChitra, K. Jayanthi, and V.Nagarajan, "Design of microstrip slotantenna for WiMAX application" IEEE International Conference onAutomation, Computing, Communication, Control and CompressedSensing (iMac4s), Mar 2013, pp 645-649.

- [12] Jobinsgeorge, shehanapareeth, multiband microstrip patch antennausing parallel slots for wireless communication, International conferenceon electrical, electronic, and optimisation techniques (ICEEOT)-2016.